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Î THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	10/771678	Docket Number	CNTR.2094	
Filed	02/04/04	Group Art Unit		
Examiner		Customer No.	23669	
Application Title	PIPELINED MICROPROCESSOR, APPARATUS, AND METHOD FOR GENERATING EARLY STATUS FLAGS			
First Named Inventor	GERARD M. COL			

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 et seq. Applicant reserves the right to swear behind or otherwise disprove the allege "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

EXPRESS MAIL LABEL NUMBER: EO 002 586 504 US

DATE OF DEPOSIT: 9/8/2004

I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 C.F.R. §1.10 on the date shown above and is addressed to Mail Stop **AMENDMENT**, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

Respectfully submitted,

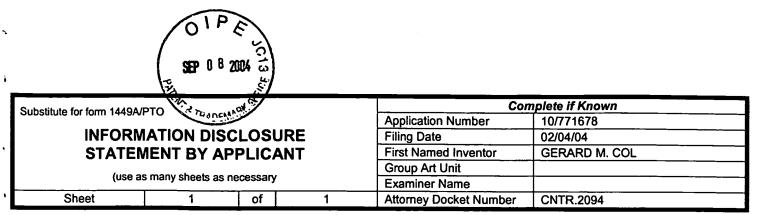
HUFFMAN PATENT GROUP, LLC

Ву

RICHARD K. HUFFMAN Reg. No. 41,082

Tel.: (719) 575-9998

Date



		NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*			T ²	
	AA	SHORT, KENNETH L. Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB. 1998. Prentice Hall. Upper Saddle River, NJ, US. Pages 16, 182		
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Examiner	Date	
	Date	
Signature	Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450